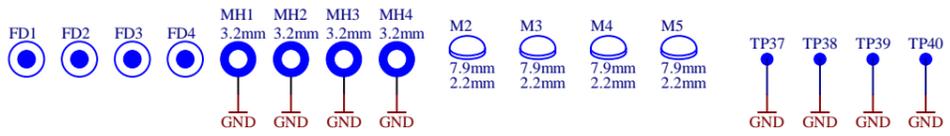
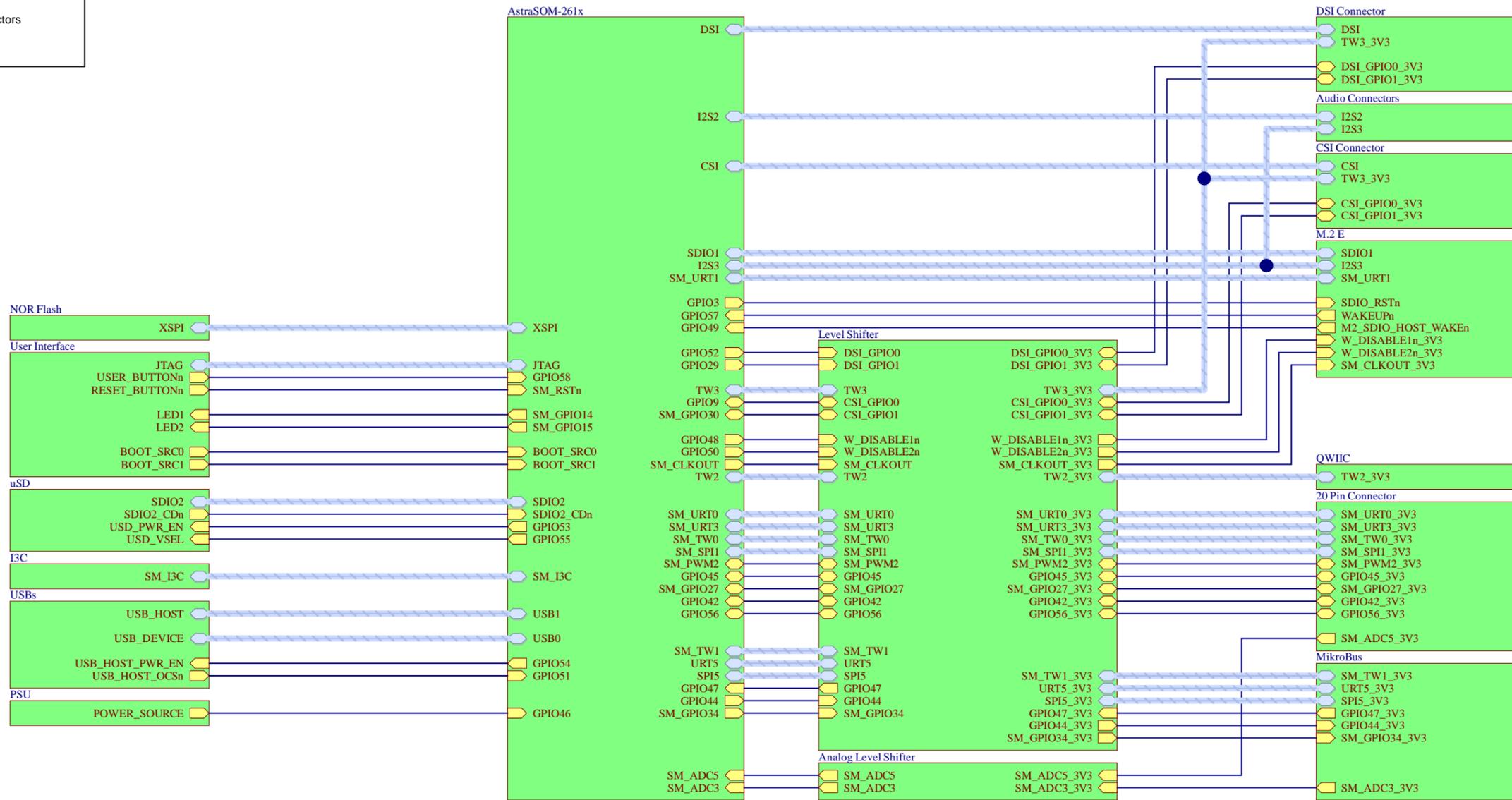


A0	Initial release
A1	- J1 changed to top contact - D8 changed
B0	- logos updated - J4 (I2S2) connector added - GPIO6 assignment changed to GPIO47 - GPIO7 assignment changed to GPIO46 - GPIO11 assignment changed to GPIO44 - JP1-JP4 added - J14 (I2S3) connector added - JTAG connector moved by 0.5mm - SM_ADC13 and SM_ADC15 exchanged - J15 (DSI) connector added - Soldermask changed to white - TP46 - TP49 added - Significant rerouting around DSI / CSI connectors - Design rules changed to 0.1mm / 0.1mm - J8's 3D model updated - 3V3 power plane moved to L2



Project		Variant [No Variations]	
Title Block Diagram		Rev.	
Size A3	Date 2/18/2026 2:57:40 PM	Sheet 1/20	